

David G. Jacobowitz

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Employment History

Pacific Gas and Electric Company, San Francisco CA

Sr. Analyst, Market Design & Analysis (Nov 2007 – present)

Tensilica, Inc., Santa Clara, CA

Senior Field Applications Engineer (Jan 2002 – Aug 2005)

Applications Engineer (Jan 2000 – Jan 2002)

Quickturn Design Systems, a division of Cadence Design Systems, San Jose, CA

Field Applications Engineer (Dec 1997 – Jan 2000)

Intel Corporation, Santa Clara, CA

Circuit Design Engineer (Jul 1995 – Dec 1997)

Internships

California Center for Innovative Transportation, Berkeley CA

Policy Analyst / Graduate Researcher (Jan 2007 – July 2007)

Federal Aviation Administration, Washington DC

Intern, Office of Policy and Plans (Jun 2006 – Aug 2006)

Skills & Experience Summary

Technical and Engineering

- Familiar with various types of quantitative modeling, including regression and Monte-Carlo methods.
- Proficient in engineering and financial analysis of photovoltaic and solar thermal systems.
- Extensive knowledge of engineering practices in the semiconductor industry, with strengths in semiconductor IP design and sales, microprocessor and DSP design, and hardware/software co-design.
- Possess more than ten years combined experience in the semiconductor, EDA, and IP industry, including roles in circuit design, logic design, corporate and field applications engineering, and marketing.
- Advanced software skills, including development in numerous software and hardware design languages (C, C++, Java, Perl, Tcl, VHDL, Verilog, various assemblers), MathCad, Matlab, STATA, Linux system administration, Internet crawler/spider-based data mining, custom simulation design. Author of an open-source photovoltaic performance simulator and a simple general-purpose statistics package.

Management

- Successful project manager and account manager.
- Participated in management leadership training while at Intel.
- Startup experience writing business plans, pitching to VC, building prototype/demonstration IT projects.

Communications

- Possess excellent verbal and written communications skills. Comfortable presenting complex and controversial material to large groups of people.
- Demonstrated ability to translate technical issues for a non-technical audience, and to do the reverse, providing concrete direction for a technical team when goals have been stated very generally.
- Skilled at persuading wary customers adopt new products despite perceived risk.
- Have demonstrated success on the order of millions of dollars in sales at a Silicon Valley startup operating in what was then an unproven business sector.

Policy & Decision Analysis

- Regularly perform various policy analyses for PG&E, including: comparing system-wide benefits of various approaches to solar power investment, cost-benefit analyses of demand-response programs, ROI analyses for solar power investments, analyses of various Community Choice Aggregation scenarios, feasibility analysis of a “green tariff,” etc.
- Supported internal PG&E efforts to understand dynamics of new ISO market system (MRTU) and to better understand and detect potential exercise of market power.
- Analyzed the FAA’s formal benefit-cost analysis guidance and provided recommendations for improvements which FAA staff may use to update their published guidance for authors of grant proposals.
- Studied public-private partnerships and their effects on innovation in the transportation sector.
- High evaluations in UCB graduate coursework in energy and transportation, including Energy and Society, Photovoltaics, California Energy Politics, and Air Transportation.
- High evaluations in general graduate coursework in microeconomics, quantitative methods, law, innovation policy, and political analysis.

Additional Skills

- Spanish: proficient reading and basic writing skills; some speaking ability.
- Aviation: Instrument-Rated Private Pilot.

Patent

“Simultaneous Real-Time Trace and Debug,” #7,080,283

Describes methods and apparatus for simultaneously ascertaining the state of numerous microprocessors collocated on a single system-on-chip.

Education

University of California, Berkeley, Goldman School of Public Policy

Master of Public Policy, May 2007

Thesis: “Investing in Solar for California; A Guide for Narrowing Options and Identifying Opportunities” (for Pacific Gas & Electric Corporation).

Winner, 2006-7 Smolensky Prize for Outstanding Policy Analysis

University of Virginia, School of Engineering and Applied Science

Bachelor of Science in Electrical Engineering, Highest Distinction, May 1995

Thesis: “Design of an Eight-Bit Microprocessor and Evaluation of Design Methodologies”